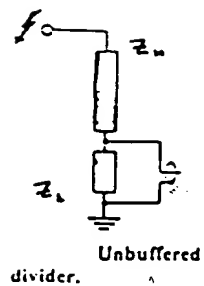
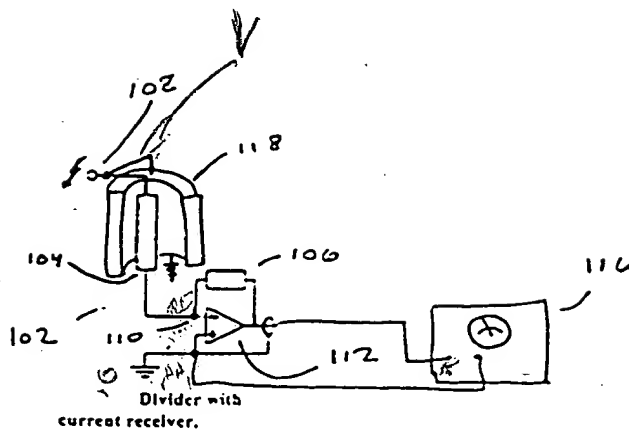


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Unbuffered divider.

FIGURE 1



Divider with current receiver.

FIGURE 2

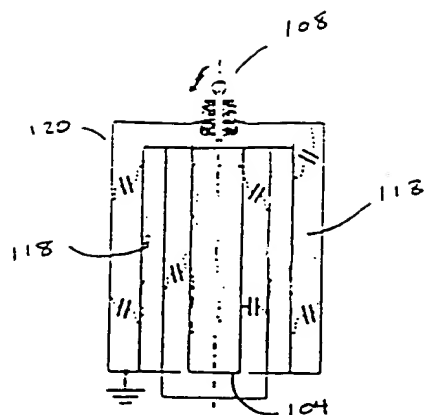


FIGURE 3

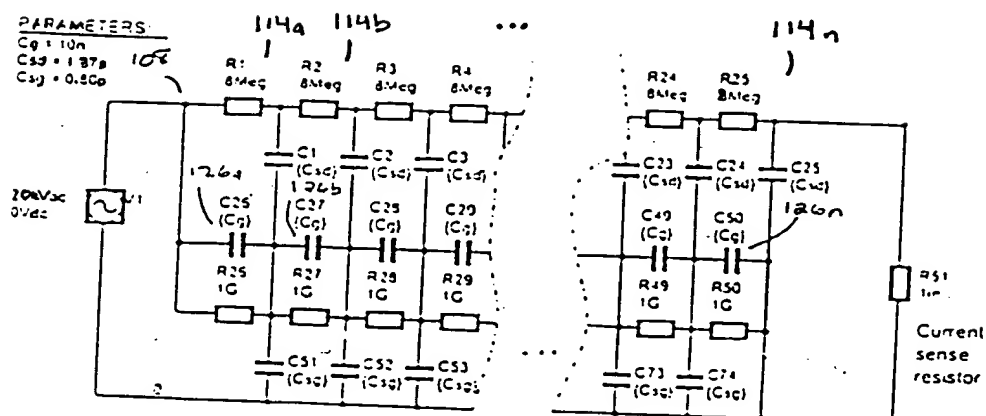


FIGURE 4

FOUO 14400

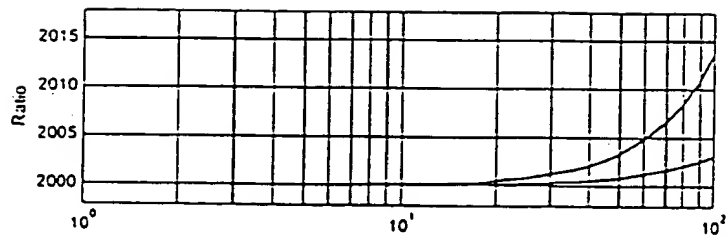


FIGURE 5

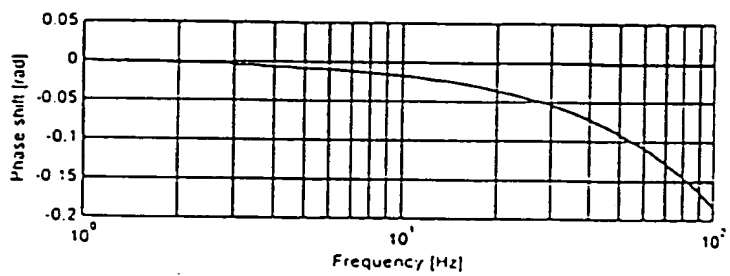
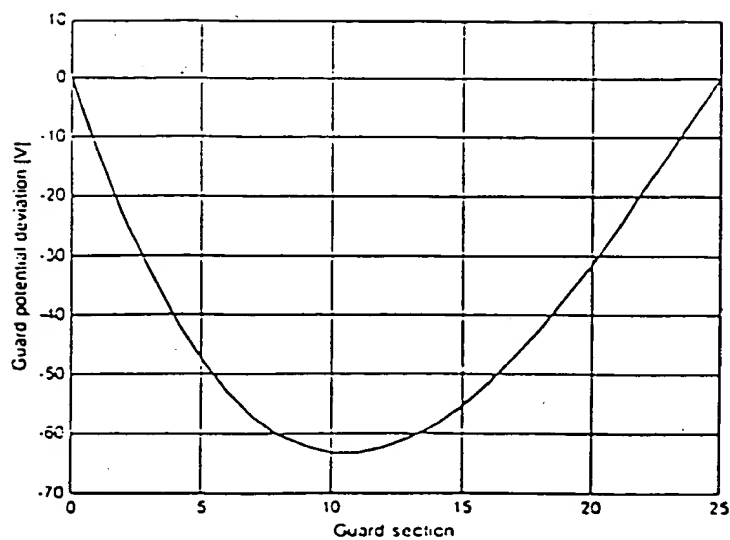


FIGURE 6

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Deviation of the guard potential from ideal potential.

FIGURE 7

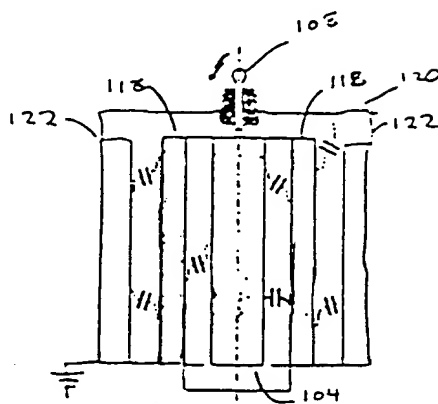


FIGURE 8

09937991-11501

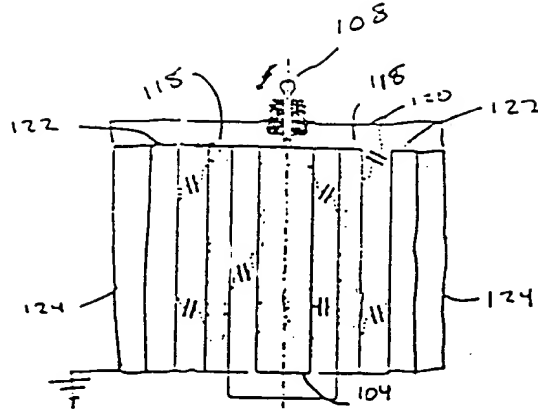


FIGURE 9

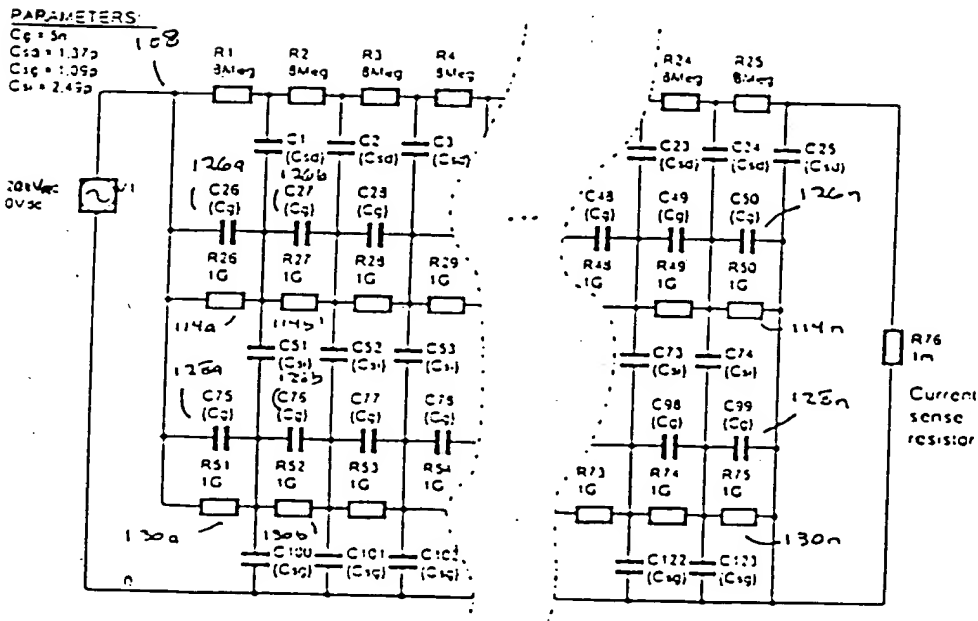
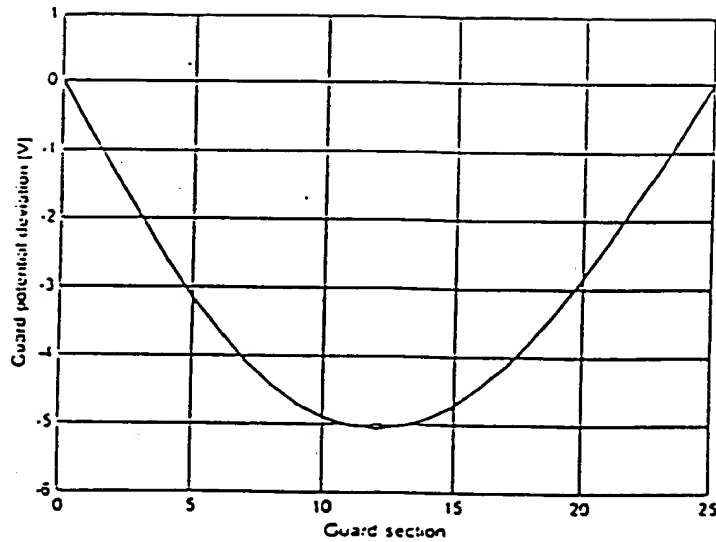


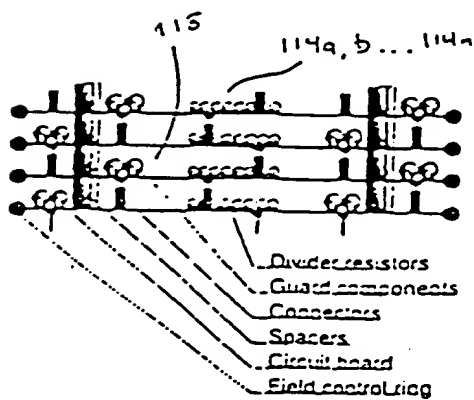
FIGURE 10

00007991 41504
FD5777 T6628660



Deviation of the inner guard potential from ideal potential.

FIGURE 11



Cross-sectional view of
four circuit boards in a divider
stack.

FIGURE 12

0987991-11504
T09777-76628650

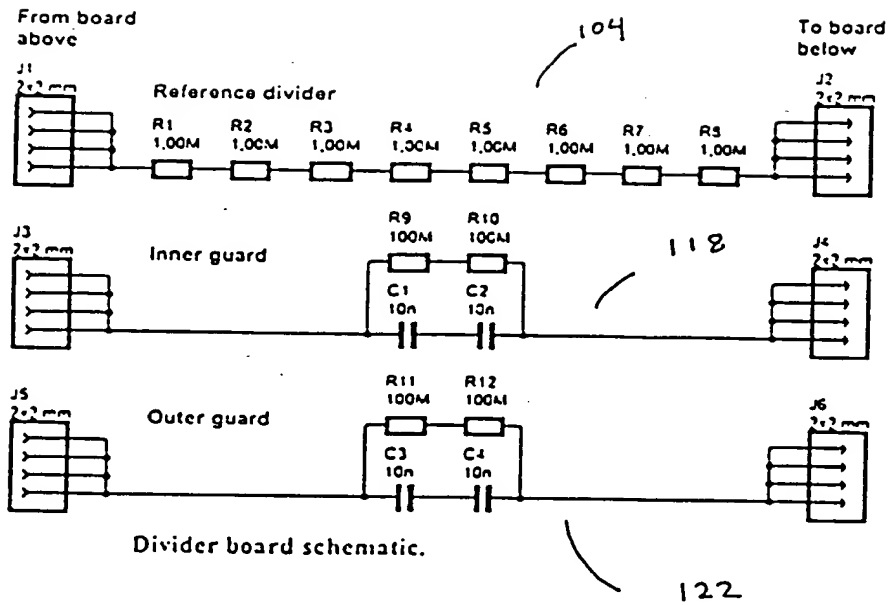


FIGURE 13

0907991-11501
TEST-T-10028660

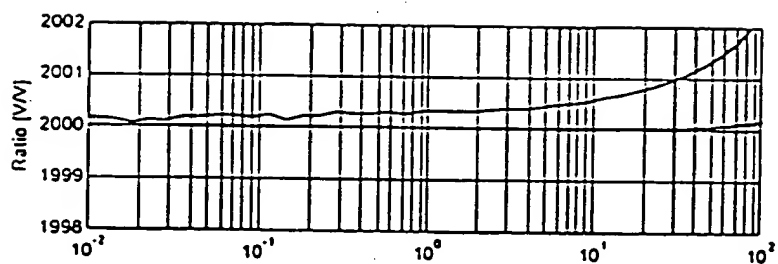


FIGURE 14

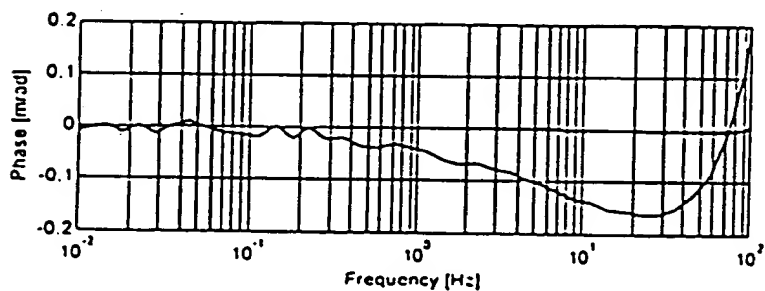


FIGURE 15

09037991-11504
FOUO T6628660

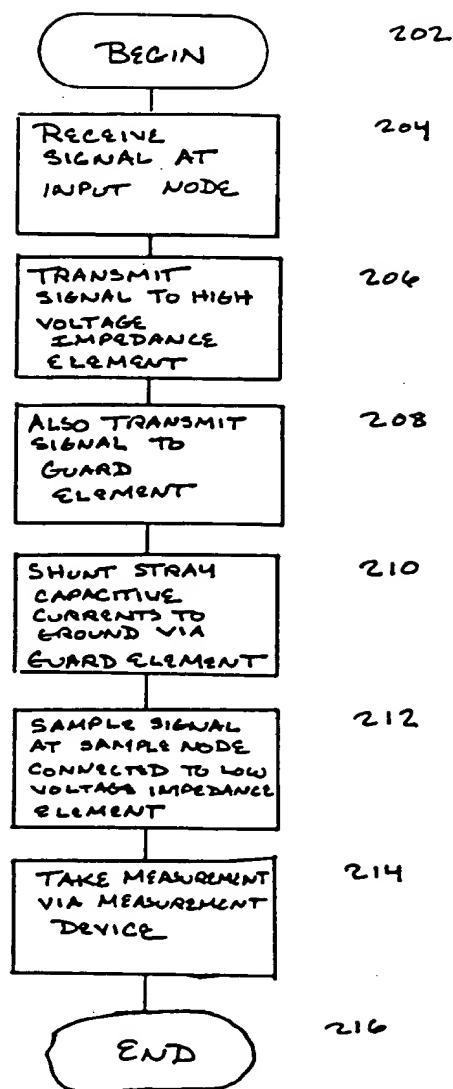


FIGURE 16